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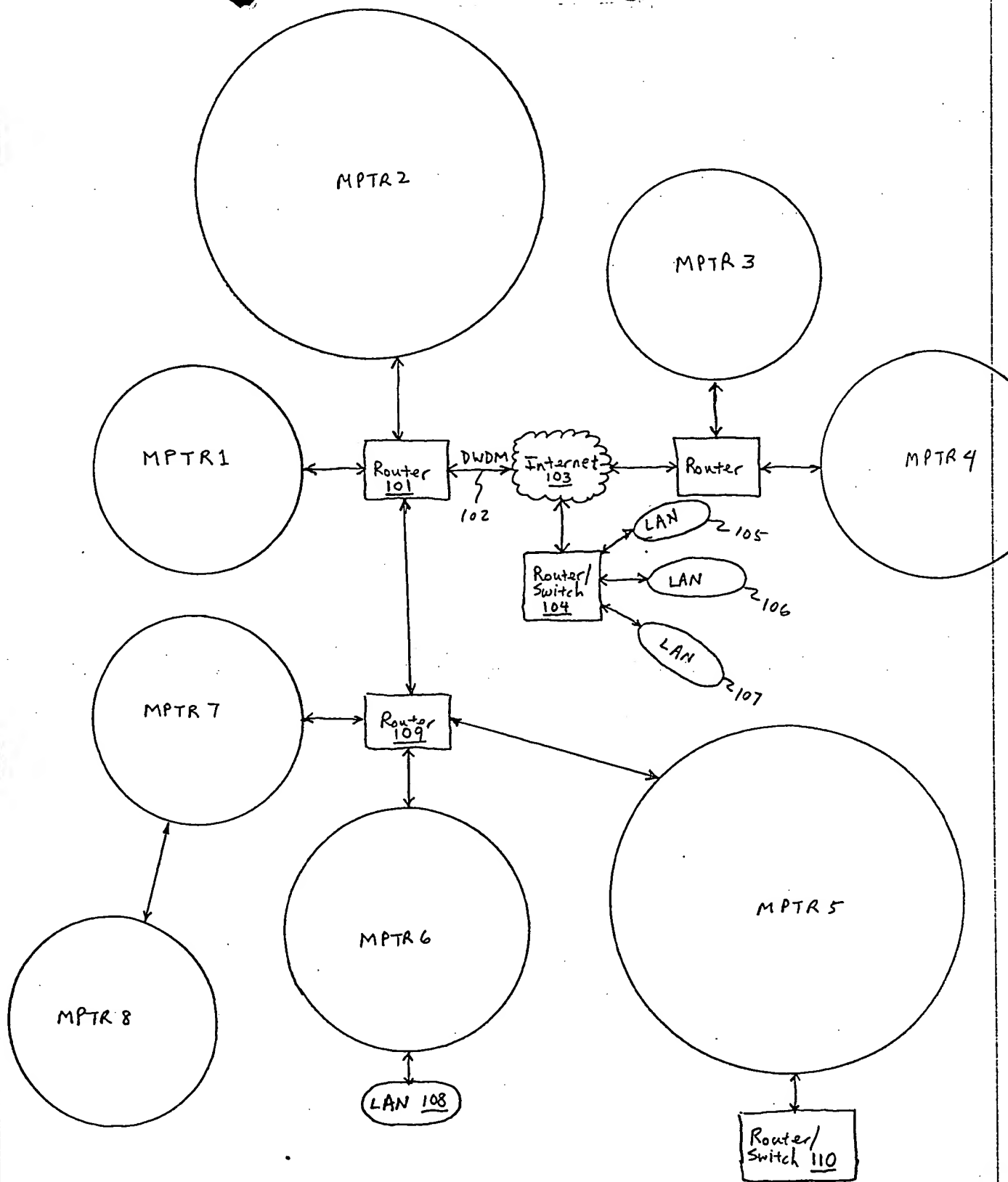


Figure 1

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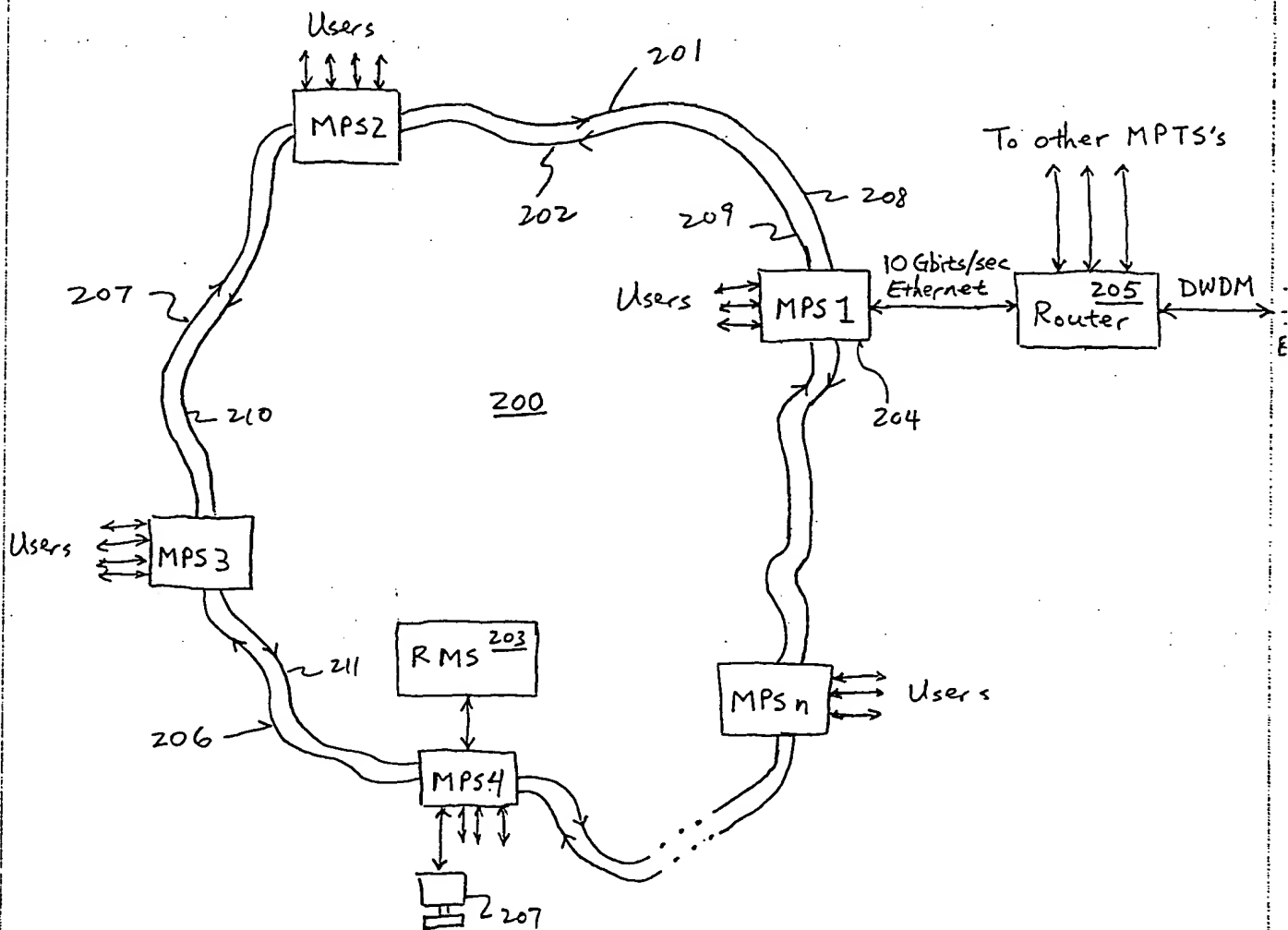


Figure 2

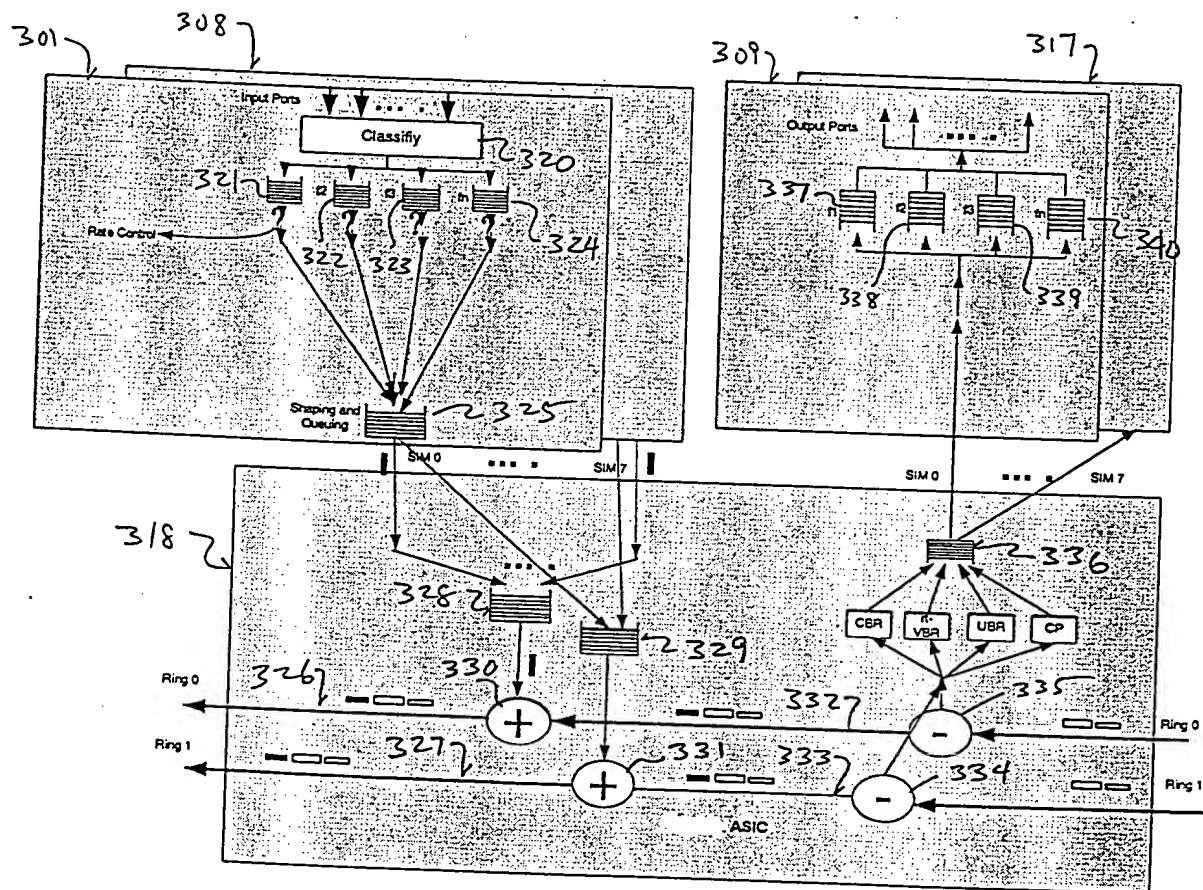


Figure 3

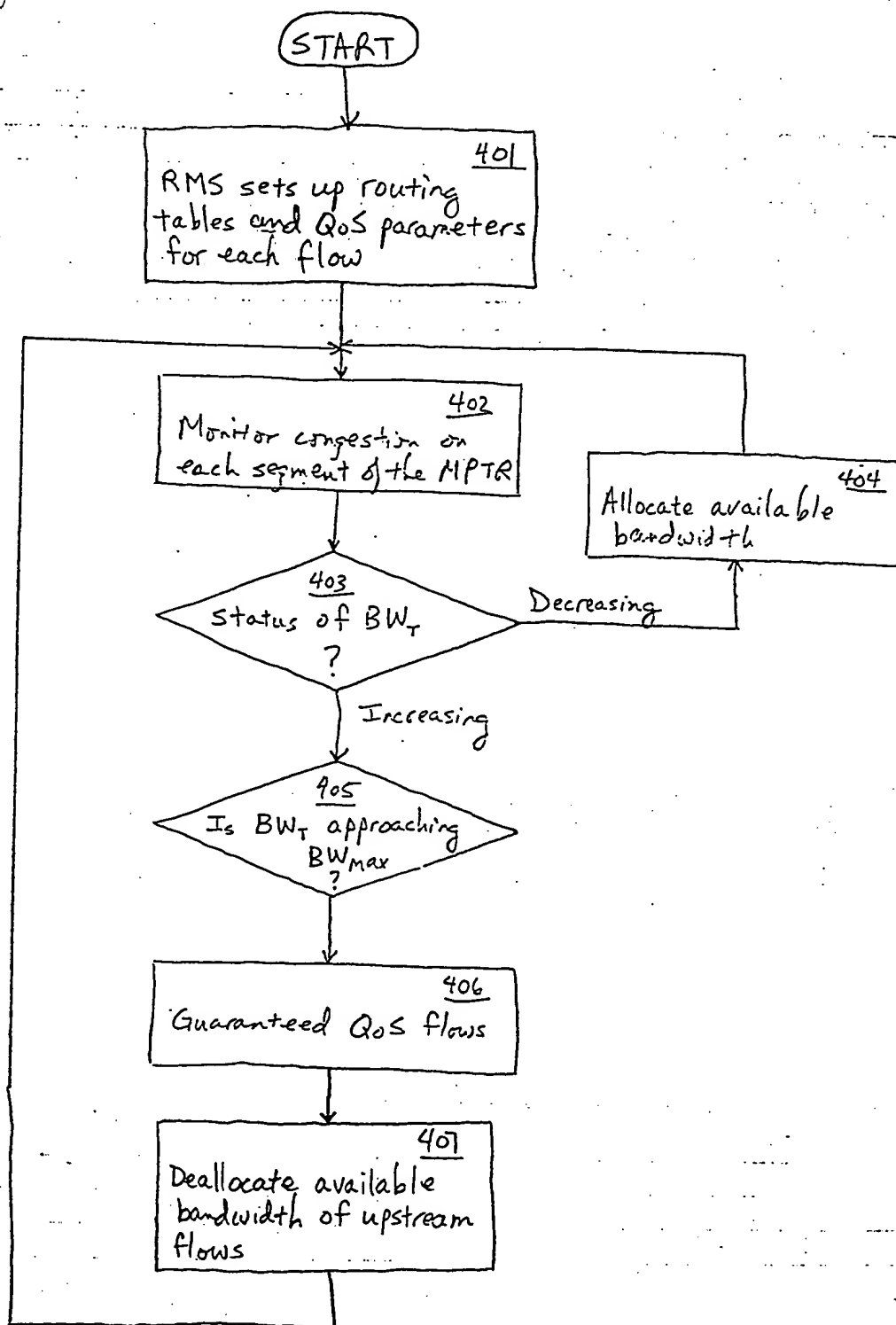
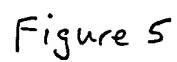


Figure 4

A block diagram of a parallel processing system. At the top, three input lines labeled 508, 509, and 510 enter from the left. Each line passes through a switch or connector block. Below these, five vertical processing units are arranged in a row. Each unit consists of a stack of five rectangular blocks, with labels 531, 532, 533, 534, and 535 pointing to the stacks from left to right. Below each stack is a valve symbol (two triangles meeting at a point). These valves are labeled 545, 546, 547, and 548 from left to right. The output of the first valve (545) is connected to a circular adder block labeled 'MPS' with a '+' sign. The outputs of the other four valves (546, 547, 548) and a fifth line from the top right (labeled 502) are connected to the same adder block. The output of the adder block is labeled 525.



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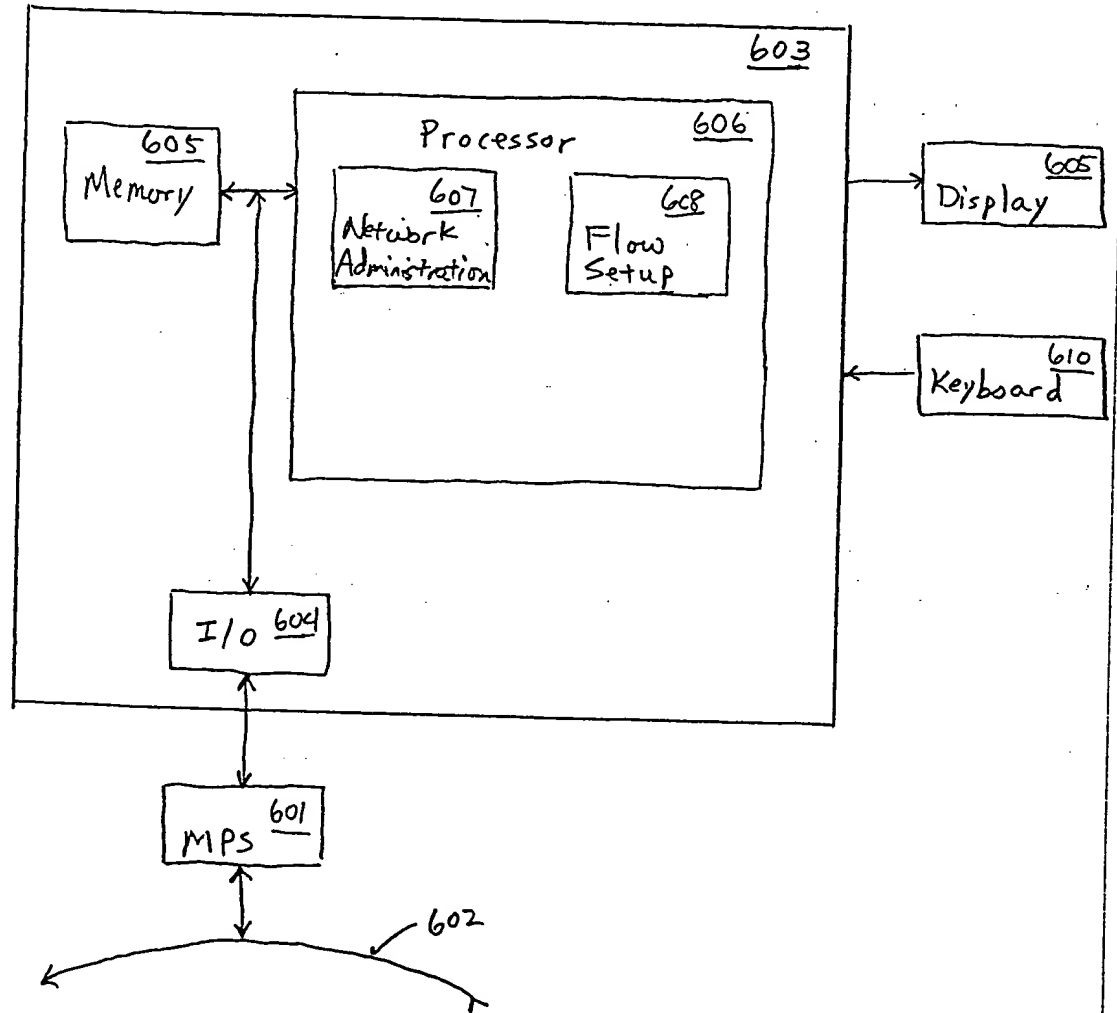


Figure 6.